

CONDUCTING A SYNTHESIS OF A DIGITAL AUTOMATON FOR AN AUTOMATED FIREFIGHTING SYSTEM

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Every year the industry in the world is gaining momentum: the number of industrial enterprises is growing, and with it the number of accidents at them. Oil today is the most common product for the synthesis and production of products. Increasing the level of fire protection systems at oil refineries remains one of the most important components of protecting people from technogenic hazards. The speed of innovation allows the application of artificial intelligence in the creation of automated fire protection systems. **Research objective.** This study is aimed at building a model of an automated integrated fire protection system (AISPPO). Through the synthesis of digital automata and minimizing the control functions of the digital model is created a system of automated integrated fire protection system. **Materials and methods.** To solve the problems of research used methods of constructing graphical algorithms of automated integrated fire protection system. This system is a new approach to solving the issue of safety of industrial facilities in the oil refining industry. **Results.** The proposed new model of the software implementation of a digital automaton in an automated integrated system of fire detection and monitoring of an oil refinery has made it possible to create a bank of calculated and analytical data on all potential types of failures in the structure of the enterprise in order to train personnel and make changes to existing methodological documents and instructions for personnel actions in a particular situation. **Conclusion.** The developed technology allows you to process the incoming signal contained in cyclograms into an intermediate form for the synthesis of digital automata using innovative tools.

Keywords: Mili automaton, digital automaton, graph, graph vertex, minimization of logic function.

Introduction

The model of a digital automaton (CA) of an automated firefighting system with input and output signals induces a one-to-one mapping of the set of commands in the input signals (the input command mapping) into the set of commands in the output signals. In this article we consider the stages of solving the problem of synthesizing automata by the mappings induced by them.

According to the developed algorithm (Fig. 1), when selecting the states, one should take into account such recommendations as:

- the correspondence of the set and the initial set;
- the choice of the next state is made according to the ascending order after each PROCESS block;
- before each DECISION block, after each line adjacency point, which indicates the transition direction [1].

Refinery accident and fire analysis

According to the developed functioning algorithm decided that the scheme of the CA model of the automated fire suppression system (AFS) will include 14 $a_0, a_1, a_2, \dots, a_{13}$ states, where a_0 – the initial state [2].

All 14 states of digital automaton will be encoded by four-bit binary numbers (Tables 1–3). The memory block, in this case, will be a four-bit parallel register on D-triggers, because storage of each bit of the binary code will use one trigger [3, 14].

Based on the developed algorithm of functioning of the digital automaton ASFT we build a graph [4]. The state of the device in the graph will depend proportionally on the values of the vertices (vertices of the graph). The vertices of the graph of the ASPT CA model are connected by arcs, which show the direction of transition. At the top of the arcs we write transition conditions and output signals [5, 11, 12].

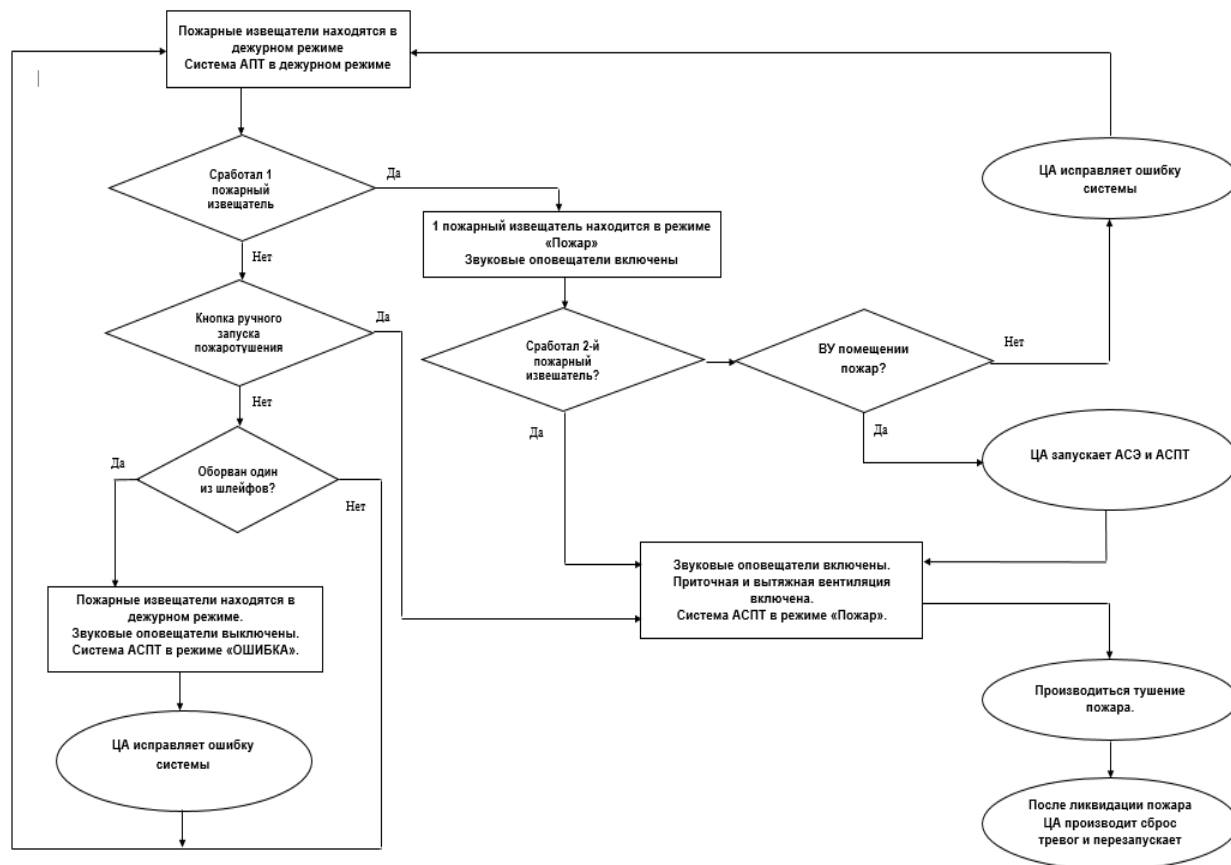


Fig. 1. Algorithm of automatic fire extinguishing system operation at an industrial facility

Table 1
Assignment of values to the states of the digital automata model of the automatic fire-fighting system

State of the machine	Status deciphering
a_0	Fire detectors are in standby mode. APT system in standby mode
a_1	1 fire detector is in Fire mode. The sounders are on
a_2	The fire detectors are in standby mode. The sounders are switched off. The ASFT system is in ERROR mode
a_3	CA corrects an error with the system
a_4	The sounders are switched on. Supply and exhaust ventilation is switched on. The ASPT system is in Fire mode
a_5	The fire is extinguished
a_6	After fire suppression, the CA resets the alarms and restarts the ASFT
a_7	CA launches ASE and ASPT
a_8	CA corrects system error
a_9	1 fire detector was triggered
a_{10}	Button for manual start of fire extinguishing is pressed
a_{11}	One of the loops is broken
a_{12}	The 2nd fire detector is triggered
a_{13}	There is a fire in the premises

Table 2

Coding of the states of the digital automata model
of the automatic firefighting system

State of the machine	Binary code			
	Q_4	Q_3	Q_2	Q_1
a_0	0	0	0	0
a_1	0	0	0	1
a_2	0	0	1	0
a_3	0	0	1	1
a_4	0	1	0	0
a_5	0	1	0	1
a_6	0	1	1	0
a_7	0	1	1	1
a_8	1	0	0	0
a_9	1	0	0	1
a_{10}	1	0	1	0
a_{11}	1	0	1	1
a_{12}	1	1	0	0
a_{13}	1	1	0	1

Table 3

Table of D-trigger transitions of the digital automatic
firefighting system model

Go to	D
0 → 0	0
0 → 1	1
1 → 0	0
1 → 1	1

Read the graph as follows: the automaton is in the initial state a_0 , then under the signal from the fire detector it changes its state to a_1 , with this transition the output signals must be formed y_1, y_5, y_6 . This is followed by a transition a_2 to the state with the formation of output signals y_1, y_3, y_6 . From the state a_2 to a_3 , then to a_4 . From the state a_4 the transition to the state a_5 , or a_8 [5] is possible. The a_5 automaton enters the state if the external condition (fire is detected) x_3 is 1 ($\overline{x_3}$) with issuing of y_1, y_3, y_4 control signals, and the automaton a_8 enters the state if the same signal is 0 ($\overline{x_3}$), etc.

After constructing the graph, fill in the table of functions of the vertices of the graph. Using this table you can write functions for any number of variables (Fig. 2). After that it is necessary to analyze it carefully in order to simplify (minimize) it, because the tabular method does not give an opportunity to obtain in perfect disjunctive normal form (DNF) for outputs the minimal disjunctive normal form (MDNF) or the minimal conjunctive normal form (MCNF) [6]. In this case it will be enough to apply the gluing law to some expressions [7, 8].

On the transition column of the digital automaton of the automated integrated firefighting system let's fill in the table 4. Example for the first line: The initial state, which is coded as "0000", changes to the state with the code "0001". This transition is unconditional. We see that $Q_4 = 0$, $Q_3 = 0$, $Q_2 = 0$, $Q_1 = 0$, and in the new state $Q_4 = 0$, $Q_3 = 0$, $Q_2 = 0$, $Q_1 = 1$ [6]. According to the table of D-trigger transitions $Q_4 = 0$, $Q_3 = 0$, $Q_2 = 0$, $Q_1 = 1$, to get D_1 , it is necessary to supply 1 to the input in the column

D_2, D_3, D_4 “Trigger Control Signals”, and to supply 0 to the others, at this transition the signals are formed y_1, y_5, y_6 . All the following lines are completed in the same way.

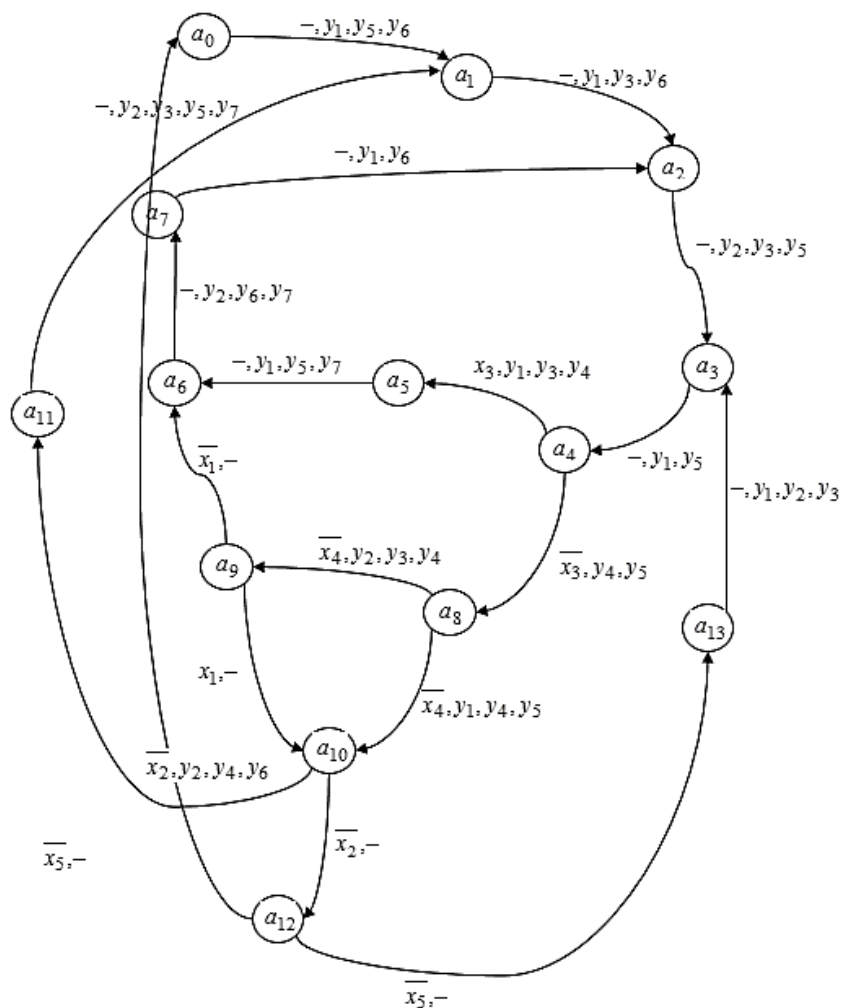


Fig. 2. Transition graph of the digital automaton of the automated integrated firefighting system

According to the table of functioning of the digital automata graph of AISPT we make analytical expressions in the SDNF for output signals y_1, y_2, y_3 , and also signals of control of triggers D_4, D_3, D_2, D_1 . The perfect disjunctive normal form of the function is a disjunction of elementary conjunctions [7, 15].

The output signal y_1 will be generated if the automaton is in state a_0 , or in a_1 , or in a_3 , or in a_5 , or in a_7 , or in a_{13} , and the sign $a_4 x_3 = 1$, or in state a_8 and the sign $x_4 = 0$. Similarly the functions for other output signals and trigger control signals are written [10, 13].

Functioning of the graph of the digital automaton ASPT

Table 4

$y_1 = a_0 \vee a_1 \vee a_3 \vee a_4 x_3 \vee a_5 \vee a_7 \vee a_8 \overline{x_4} \vee a_{13}$	(1)
$y_2 = a_2 \vee a_6 \vee a_8 x_4 \vee a_{10} \overline{x_2} \vee a_{11} \vee a_{13}$	(2)
$y_3 = a_1 \vee a_2 \vee a_4 x_3 \vee a_8 x_4 \vee a_{11} \vee a_{13}$	(3)
$y_4 = a_4 x_3 \vee a_4 \overline{x_3} \vee a_8 x_4 \vee a_8 \overline{x_4} \vee a_{10} \overline{x_2} = a_4 \vee a_8 \vee a_{10} \overline{x_2}$	(4)
$y_5 = a_0 \vee a_2 \vee a_3 \vee a_4 x_3 \vee a_5 \vee a_8 x_4 \vee a_{11}$	(5)

$y_6 = a_0 \vee a_1 \vee a_6 \vee a_7 \vee a_{10}x_2$	(6)
$y_7 = a_5 \vee a_6 \vee a_{11}$	(7)
$D_1 = a_0 \vee a_2 \vee a_4x_3 \vee a_6 \vee a_8x_4 \vee a_{10}x_2 \vee a_{11} \vee a_{12}x_5 \vee a_{13}$	(8)
$D_2 = a_1 \vee a_2 \vee a_5 \vee a_6 \vee a_7 \vee a_8x_4 \vee a_9x_1 \vee a_9x_1 \vee a_{10}x_2 \vee a_{13} =$ $= a_1 \vee a_2 \vee a_5 \vee a_6 \vee a_7 \vee a_8x_4 \vee a_9 \vee a_{10}x_2 \vee a_{13}$	(9)
$D_3 = a_3 \vee a_4x_3 \vee a_5 \vee a_6 \vee a_9x_1 \vee a_{10}x_2 \vee a_{10}x_2 \vee a_{12}x_5 =$ $= a_3 \vee a_4x_3 \vee a_5 \vee a_6 \vee a_9x_1 \vee a_{10} \vee a_{12}x_5$	(10)
$D_4 = a_4x_3 \vee a_8x_4 \vee a_8x_4 \vee a_9x_1 \vee a_{10}x_2 \vee a_{10}x_2 \vee a_{12}x_5 =$ $= a_4x_3 \vee a_8 \vee a_9x_1 \vee a_{10} \vee a_{12}x_5$	(11)

Formulas (4), (9), (10), and (11) have been simplified using the gluing law. Using the laws of double negation and de Morgan formulas, the initial expressions from the basis of AND, OR, NOT are converted to the basis of AND, NOT.

$y_1 = a_0 \wedge a_1 \wedge a_3 \wedge a_4x_3 \wedge a_5 \wedge a_7 \wedge a_8x_4 \wedge a_{13}$	(12)
$y_2 = a_2 \wedge a_6 \wedge a_8x_4 \wedge a_{10}x_2 \wedge a_{11} \wedge a_{13}$	(13)

Convert all other formulas by analogy. With the help of the Logic Converter from the MultiSIM simulator program we will minimize the logic functions, which determine each of the control signals of the KS 1 triggers according to Table 4. The results of the minimization of the logic functions are shown in the figures (Figs. 3–15) [9].

$$J1 = \overline{Q_1}Q_2Q_3;$$

$$K1 = \overline{Q_2}Q_3Q_4 + Q_2\overline{Q_3}Q_4;$$

$$J2 = \overline{Q_1}Q_3 + \overline{Q_1}Q_3\overline{Q_4};$$

$$K2 = \overline{Q_1}Q_3 + Q_1\overline{Q_3}Q_4;$$

$$J3 = \overline{Q_1} + \overline{Q_2};$$

$$K3 = \overline{Q_1} + \overline{Q_2};$$

$$J4 = Q_1Q_2\overline{Q_3}Q_4;$$

$$K4 = Q_1\overline{Q_2}Q_3.$$

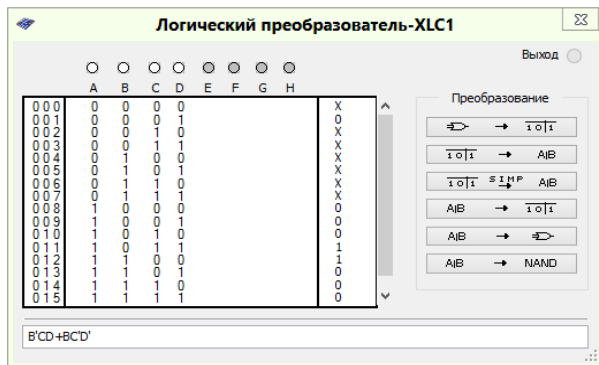


Fig. 3. Minimization of the logic function to control trigger signals K1

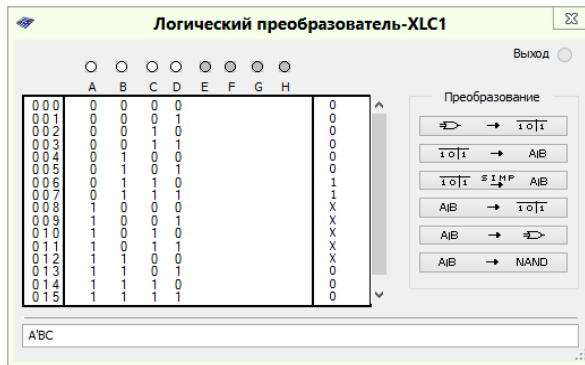


Fig. 4. Minimization of the logic function to control the trigger signals J1

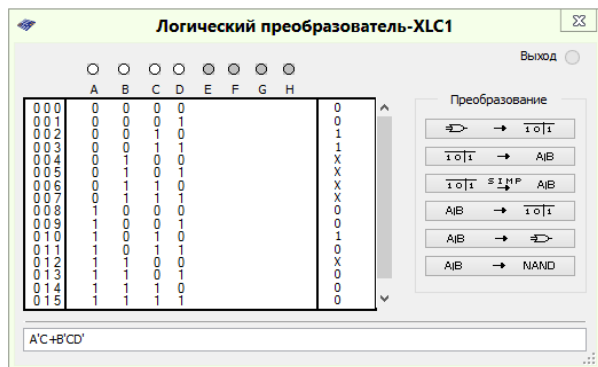


Fig. 5. Minimization of the logic function to control J2 trigger signals

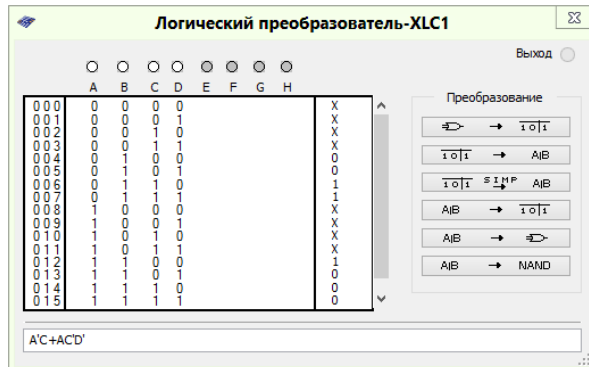


Fig. 6. Minimization of the logic function to control trigger signals K2

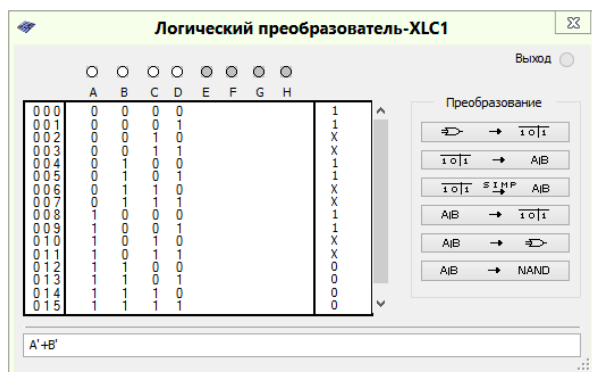


Fig. 7. Minimization of the logic function to control J3 trigger signals

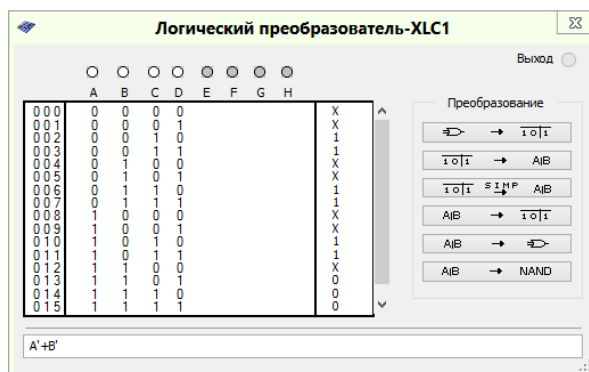


Fig. 8. Minimization of the logic function for the control of trigger signals K3

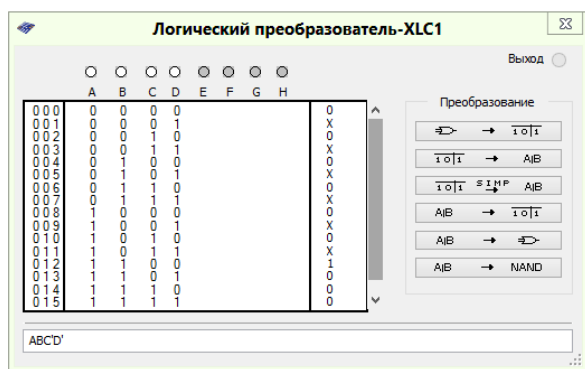


Fig. 9. Minimization of the logic function to control the trigger signals J4

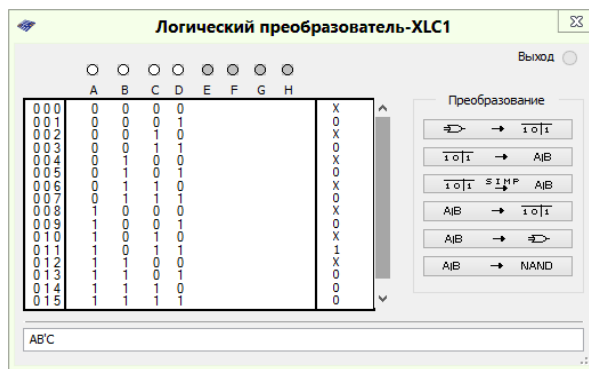


Fig. 10. Minimization of the logical function for the control with the K4 trigger signals

Now let's minimize the logical functions of KS 2, using also Logic Converter from the MultiSIM simulator. The result of minimization of logical functions (Figs. 11–15).

$$\begin{aligned}
 Y1 &= \overline{Q_1} \overline{Q_2} \overline{Q_3} \overline{Q_4} + Q_1 \overline{Q_2} \overline{Q_3} \overline{Q_4}; \\
 Y2 &= \overline{Q_1} \overline{Q_2} \overline{Q_3} \overline{Q_4} + Q_1 \overline{Q_2} \overline{Q_4} + Q_1 \overline{Q_3} \overline{Q_4}; \\
 Y3 &= \overline{Q_1} \overline{Q_2} \overline{Q_4} + Q_1 \overline{Q_3} \overline{Q_4}; \\
 Y4 &= \overline{Q_1} \overline{Q_2} \overline{Q_3} \overline{Q_4} + \overline{Q_1} \overline{Q_2} \overline{Q_4} + Q_1 \overline{Q_2} \overline{Q_3} \overline{Q_4} + Q_2 \overline{Q_3} \overline{Q_4}; \\
 Y5 &= \overline{Q_1} \overline{Q_3} \overline{Q_4} + Q_2 \overline{Q_3} \overline{Q_4} + Q_1 \overline{Q_2} \overline{Q_3} \overline{Q_4} + Q_1 \overline{Q_2} \overline{Q_3} \overline{Q_4}.
 \end{aligned}$$

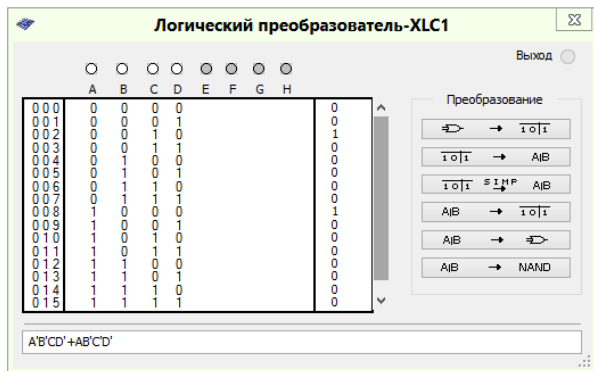


Fig. 11. Minimization of the logic function to control the signals of trigger Y1

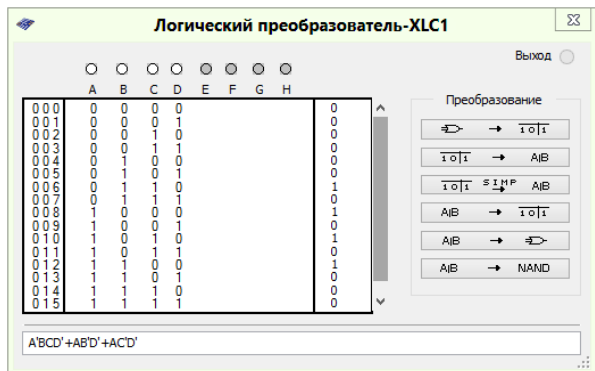


Fig. 12. Minimization of the logic function for the Y2 trigger signal control

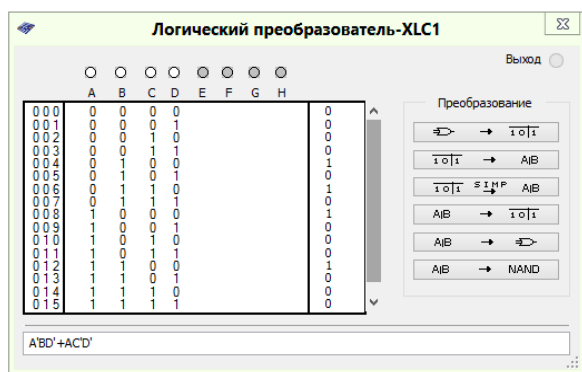


Fig. 13. Minimization of the logic function for the Y3 trigger signal control

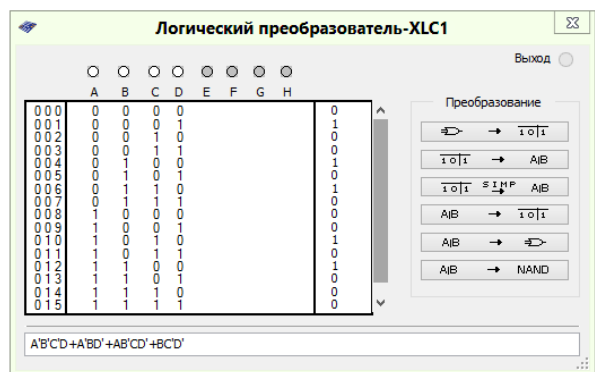


Fig. 14. Minimization of the logic function to control the signals of trigger Y4

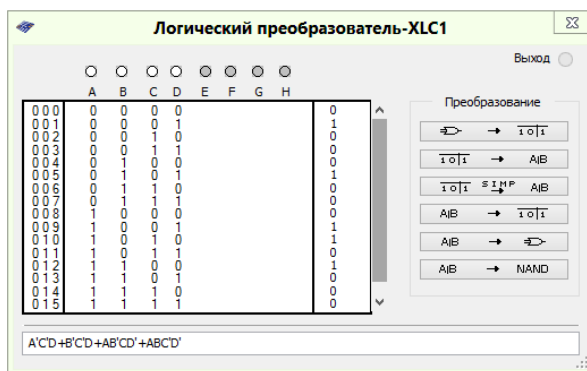


Fig. 15. Minimization of the logic function to control the trigger signals Y5

Conclusion

Minimized logic functions for KS 1 and KS 2 will be used in the construction of the model of digital automata of automated integrated fire protection system.

To simplify the CA scheme the minimized logic functions for KS 1 and KS 2 are analyzed and the same logic functions are defined. On the basis of the functional diagram of CA its circuit diagram on the selected series of digital integrated circuits is built.

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ПРОВЕДЕНИЕ СИНТЕЗА ЦИФРОВОГО АВТОМАТА ДЛЯ АВТОМАТИЗИРОВАННОЙ СИСТЕМЫ ПОЖАРОТУШЕНИЯ

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Повышение уровня систем противопожарной защиты НПЗ остается одной из важнейших составных частей обеспечения защиты населения от угроз техногенного характера. Скорость развития инноваций позволяет применить искусственный интеллект при создании автоматизированных систем пожарной безопасности. **Цель исследования.** Данное исследование на-

правлено на построение модели автоматизированной интегрированной системы управления противопожарной защитой (АИСУПЗ). **Материалы и методы.** Для решения задач исследования использованы методы построения графов, задание графов алгоритмом работы автоматизированной интегрированной системы противопожарной защиты. Данная система является новым подходом к решению вопроса безопасности промышленных объектов нефтеперерабатывающей отрасли. **Результаты.** Предложенная новая модель программной реализации цифрового автомата в автоматизированной интегрированной системе обнаружения и мониторинга пожара нефтеперерабатывающего предприятия дала возможность создать банк расчетных и аналитических данных по всем потенциально возможным видам разрушения конструкции установок с целью подготовки персонала и внесения изменений в действующие руководящие документы и инструкции по действиям персонала в конкретной ситуации. **Заключение.** Разработанная технология дает возможность обработки поступающего сигнала, содержащегося на циклограммах, в промежуточную форму для синтеза цифровых автоматов при помощи инновационных инструментальных средств.

Ключевые слова: автомат Мили, цифровой автомат, граф, вершина графа, минимизация логической функции.

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